

ECE DEPARTMENT TIME TABLE
(EC PG 1st Semester) Aug-Dec 2020

ECE DEPARTMENT TIME TABLE

Period	1	2	3	4		5	6	7	8
Day/TIME	8.30-9.30	9.30-10.30	10.30-11.30	11.30-12.30		13.30-14.30	14.30-15.30	15.30-16.30	16.30-17.30
Monday	1EC601 (S.SAHA)			1EC621 (RR)	LUNCH	1EC603 (RR)			
Tuesday	1EC601 (S.SAHA)			1EC632 (RBK)			1EC634 (BG)	1EC621 (RR)	
Wednesday	1EC601 (S.SAHA)			1EC632 (RBK)		1EC602 (RKA)	1EC634 (BG)		
Thursday				1EC632 (RBK)		1EC602 (RKA)	1EC634 (BG)	1EC621 (RR)	
Friday				1EC602 (RKA)					

1EC601-Probability, Random variable and Stochastic Process-Seemanti Saha(S.SAHA)

1EC602-Data Communication and Networks-Rajeev Kumar Arya(RKA)

1EC634-Satellite Communication-Bharat Gupta(BG)

1EC621-Coding Theory-Rakesh Ranjan(RR)

1EC632-Advanced Antenna Engineering-Rabindra Kumar(RBK)

1EC603-Advanced Electronics Lab -I-Rakesh Ranjan(RR)

Time Table Co-ordinator
Dr. A. K. Bhandari

ECE DEPARTMENT TIME TABLE
(EC PG-VLSI 1st Semester) Aug-Dec 2020

ECE DEPARTMENT TIME TABLE

Conference Room ECE Department

Period	1	2	3	4		5	6	7	8
Day/TIME	8.30-9.30	9.30-10.30	10.30-11.30	11.30-12.30		13.30-14.30	14.30-15.30	15.30-16.30	16.30-17.30
Monday	1EC661 (S.SINGH)	1EC697 (S.SINGH)	1EC673 (BCN)	1EC690 (BCN)	LUNCH	1EC663 (S.SINGH)			
Tuesday	1EC662 (MP)	1EC697 (S.SINGH)		1EC673 (BCN)		1EC663 (S.SINGH)			
Wednesday	1EC661 (S.SINGH)	1EC662 (MP)	1EC673 (BCN)	1EC690 (BCN)					
Thursday	1EC661 (S.SINGH)			1EC690 (BCN)					
Friday	1EC662 (MP)	1EC697 (S.SINGH)							

1EC661-MOS Device Physics and Modelling-Sangeeta Singh(S.SINGH)
1EC662-Analysis and Design of Digital Integrated Circuits-Meena Panchore(MP)
1EC673-Low Power VLSI Design-Bal Chand Nagar(BCN)
1EC697-Organic Electronics-Sangeeta Singh(S.SINGH)
1EC690-RF IC Design-Bal Chand Nagar(BCN)
1EC663-Digital Design Lab-Sangeeta Singh(S.SINGH)

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 Dr. A. K. Bhandari