

Electronics & ICT Academy Training Program

ON

VLSI & Embedded System

(December 9-18, 2015)



Patron

Prof. (Dr.) Asok De

Director, NIT Patna

Coordinators

Dr. Md. Waseem Akram

Department of ECE, NIT Patna

&

Dr. Gaurav Kaushal

Department of ECE, NIT Patna

Supported by

Department of Electronics and Information Technology Ministry of Communications and IT, Govt. of India.

Our Partners

1. Cadence Design Systems, India.
2. CoreEL Technology, India.
3. Cadre Design Systems, India.
4. Texas Instruments, India.
5. Keysight Technology, India.
6. TI Centre for Embedded Product Design, NSIT, India.

Organized by

Electronics and ICT Academy,
National Institute of Technology
Patna, Patna- 800005 (India).

www.nitp.ac.in

Advisory committee

1. Prof. D. K. Singh, Dept. of ECE
2. Prof. M. P. Tripathi, Dept. of ECE
3. Dr. Jayanta Ghosh, Dept. of ECE

Organizing committee

1. Dr. A. K. Bhandari, Dept. of ECE
2. Dr. P. K. Kumar, Dept. of ECE
3. Dr. Rakesh Ranjan, Dept. of ECE
4. Dr. P. Mondal, Dept. of ECE
5. Mr. B. C. Sahana, Dept. of ECE

About NIT Patna

National Institute of Technology Patna is the 18th National Institute of Technology created by the Ministry of H.R.D. Government of India. The institute has highly qualified faculty of higher caliber in various disciplines. Institute offers B.Tech, M.Tech & Ph.D programmes in respective field of Engineering, Sciences and Technology with well-equipped laboratories.

The institute is situated on the south bank of holy river Ganga behind Gandhi Ghat. The Institute campus is 8 km from the Patna Junction railway station and 15 km from the Jai Prakash Narayan International Airport Patna.

The department of Electronics & Communication Engg. Offers B.Tech (ECE), M.Tech (ECE), and Ph.D. programs in various electronics related subjects.

Electronics and ICT academy

The Electronics and ICT academy is faculty development training program under the Department of Electronics

and Information Technology, ministry of communication & IT, ministry of India. The objective of the above program is to set up seven (07) Electronics and ICT Academies as a unit in IITs, NITs, IIITs etc. for faculty/mentor development/up gradation to improve the employability of the graduates/diploma holders in various streams, through collaboration of States/Union Territories with financial assistance from the Central Government.

About the training program

The objective of the VLSI Design & Embedded Systems training program is to address the research, development and design problems and advance their solutions in VLSI circuits for embedded system and ubiquitous computing applications. The various Topics to be covered in the course are:

VLSI Design

- Nanoscale Devices Modelling.
- CMOS: Circuit Design and Layout.
- Design issues at the layout, transistor, and logic level.
- TCAD simulation of Nanoscale Devices, Circuits and their reliability issues.
- The design of Advanced CMOS Memories.
- Digital System Design Using Verilog-HDL.
- FPGA based System Design.

Embedded system

- Microcontroller Organization and Architecture (ARM).
- Problem solving and Algorithm Development.
- Assembling/Compiling and Execution.

- Assembly and C Programming.
- Analysis of timing and memory requirements.
- Embedded system applications.

Resource Persons

- Prof. Mohd. Hasan (AMU, Aligarh)
- Prof. Sudeb Dasgupta (IIT Roorkee)
- Prof. A. B. Bhattacharyya (NIT Durgapur)
- Prof. Dhananjay V. Gadre (NSIT Delhi)
- Mr. Piyush (Cadence, Bangalore, India)
- Mr. Balachandra (CoreEL Technology, Bangalore, India)
- Mr. Amit Saini (Cadre Design Systems, India)
- Mr. Siwanand Singh (Keysight Technology, India)
- Dr. Gaurav Kaushal (NIT Patna)
- Dr. M. W. Akram (NIT Patna)

Who Can Participate

Faculty members of UGC/AICTE recognized Institutes. Institutes from the States Bihar, West Bengal, Jharkhand, and Odisha.

Registration and Fee Particulars

Registration form can be downloaded from the website: www.nitp.ac.in which should be filled by the candidate, and registration fee can be deposited with either DD, or NEFT. The DD must be drawn in favour of, "Director, NIT Patna" payable at Patna.

The filled Registration form along with DD should be sent to Dr. Md. Waseem Akram, Dept. of ECE, National Institute of Technology Patna, Ashok Rajpath, Patna-800005, Bihar, India.

Those who are paying the registration fee through NEFT (Bank details are given), they need to send the scan copy of bank transaction details along

with scan copy of the filled registration form to mwakram@nitp.ac.in.

The selection will be done on First Come First Serve basis up to 8th December. Registration is also possible by paying the registration fee in cash at the registration counter up to 9th December, 10.00 A.M.

Bank Details

Account number:- 20353663911
 IFSC code:- ALLA0212286
 Bank name:- Allahabad Bank, NIT Patna
 Account holder name:- Registrar NIT Patna

Registration Fee

Faculty : Rs. 2000
 Industry persons: Rs. 2500

Registration fee includes Registration kit, Lunch, Tea and a Course Completion Certificate. **Certificate will be given by Electronics & ICT Academy NIT Patna.**

Faculty member will be provided with a TI's TIVA Launchpad Evaluation Kit once the participating faculty registers with the myTI program (my.ti.com).

Accommodation

Accommodation may be provided as per the availability with nominal charges.

Address for Correspondence

Dr. Md. Waseem Akram, Dept. of ECE, National Institute of Technology Patna, Patna-800005.
 Mob. No.: +91-9760984755
 Email: mwakram@nitp.ac.in

Dr. Gaurav Kaushal, Dept. of ECE, National Institute of Technology Patna, Patna-800005.
 Mob. No.: +91-7070895548
 Email: kaushalg@nitp.ac.in

Electronics & ICT Academy Training Program

ON

VLSI & Embedded System

(December 9-18, 2015)

REGISTRATION FORM

1. Name (Block Letters) _____

2. Educational Qualification: _____

3. Designation: _____

4. Name of the institute: _____

5. Address for Communication: _____

6. E-mail: _____

7. Phone/Mobile: _____

8. D.D No./NEFT details (UTR number): _____

Date: _____

Bank name: _____

Amount (Rs.) _____

Date:

Signature: