Recruitment of Guest Faculty on Contract Basis under SMDP-C2SD Project

Walk-in-Interview for the following post (on Contract Basis) under the project "Special Manpower Development Program for Chips to System Design (SMDP-C2SD)" sponsored by MeitY (Govt. of India), New Delhi will be held on 11th February 2020 at 10:00 AM in the Department of ECE, NIT Patna.

Eligibility Criterion:

<table>
<thead>
<tr>
<th>Sl. No</th>
<th>Post</th>
<th>Qualification</th>
<th>No. of Posts / Duration</th>
<th>Consolidated Pay (Per Month)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Guest Faculty</td>
<td>M.Tech in VLSI / VLSI &amp; Embedded Systems or its equivalent with first division and B.Tech in Electronics and Communication Engineering with first division. Candidates having sound knowledge on FPGA implementation will be given preference.</td>
<td>1 Post / 1 Year OR up to the completion of the project whichever is earlier</td>
<td>Rs. 40,000/-</td>
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The appointment will be done on a contract basis for an initial period of one year and is likely to be extended based on performance. Interested candidates should carry the original certificates, a copy of resume mentioning educational qualifications, DOB, professional experience (if any), two references, contact details including email and phone numbers, etc. and a photocopy set of all documents.

An advance copy of resume (by email) may be forwarded to the Principal Investigator at the address given below.

Note:

1. An applicant has to ensure the authenticity of the information provided in support of experience claimed, other documents and photograph (ID proof).
2. No TA/DA will be paid for appearing in the Interview.
3. No-Objection certificate has to be submitted by the candidates who are already working.
4. List of selected candidate will be displayed on the Institute website.

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